

**AMENDMENTS****Amendments to the Claims:**

Please replace the claims with the following listing of claims.

1. (Currently Amended) A computer system in which a plurality of host computers and a plurality of other devices are interconnected by SCSI (Small Computer System Interface), comprising:  
a plurality of host computers and one or more other devices interconnected by SCSI  
wherein one of said host computers has a device ID identical to a device ID of one of said other devices; and  
inputting a reset signal to a SCSI control bus reset input of said one of said other devices in response to a terminal power of said one of said host computers being active,  
wherein said reset signal inputting means comprises an AND gate receiving at least said terminal power of said one of said host computers, and an OR gate having a first input receiving the output of said AND gate, and a second input receiving a reset signal of a SCSI bus.
2. (Canceled)
3. (Currently Amended) The computer system of claim 2, further comprising a latch circuit arranged between said AND gate and said OR gate, and wherein said reset signal is continuously supplied by said latch circuit.
4. (Original) The computer system of claim 1, wherein said other devices comprise hard disk drives.

5. (Currently Amended) A bus interface for the SCSI standard to which a plurality of host computers and a plurality of other devices are connected, comprising:

a plurality of host computers and one or more other devices interconnected by SCSI wherein a device ID of one of said host computers is identical to a device ID of one of said other devices; and  
outputting a reset signal to a reset terminal of said bus interface connected to said one of said other devices in response to a terminal power of said one of said computers being active; and  
an AND gate receiving at least said terminal power of said one of said host computers,  
and an OR gate having a first input receiving the output of said AND gate, and a  
second input receiving a reset signal of a SCSI bus.

6. (Canceled)

7. (Currently Amended) The bus interface of ~~claim 6~~ claim 5, wherein a latch circuit is arranged between said AND gate and said OR gate, and said reset signal is continuously supplied by said latch circuit.

8. (Currently Amended) A method for controlling a bus interface conforming to the SCSI standard to which a plurality of host computers and a plurality of other devices are connected, comprising:

determining whether a terminal power of one of a plurality of host computers interconnected to one or more other devices by SCSI is active when a device ID of said one of said host computers is identical to a device ID of one of said other devices; and

outputting a reset signal to a reset terminal of said bus interface connected to said one of said other devices when said terminal power is active, wherein said terminal power of said one of said host computers is input to an AND gate, the output of said AND gate is input to one input of an OR gate via a latch circuit, a signal from

a reset control bus of said bus interface is input to another input of said OR gate,  
and the output of said OR gate is continuously supplied to a reset input of said one  
of said other devices.

9. (Canceled)
10. (Original) The method of claim 8, wherein said other devices are hard disk drives.
11. (Original) The method of claim 8, further comprising said one of said other devices disengaging from the bus interface upon receiving the reset signal.
12. (Original) The method of claim 8, wherein said other devices are selected from the list consisting of hard-disk drives, CD-ROM drives, WORM drives, and Bernoulli Drives.
13. (Original) A system for controlling a bus interface conforming to the SCSI standard and to which a plurality of host computers and other devices are connected, the system comprising:
  - a first host computer operating on a bus interface conforming to the SCSI standard and having a first device ID;
  - a host power terminal configured to provide power to the first host computer;
  - a device having a second device ID, the device configured to receive a reset signal and thereby disengage from the bus interface; and
  - a conflict resolution module configured to provide a reset signal to the device having a second device ID upon receiving notice of a conflict between the first device ID and the second device ID and after determining that the first host computer is receiving power from the host power terminal.
14. (Original) The system of claim 13, wherein the conflict resolution module comprises:
  - an AND gate configured to receive a first and second terminal power signal and thereby provide a powered state signal;

a latch configured to receive the powered state signal and thereby provide a powered signal that is persistent; and  
an OR gate configured to receive a reset input and to receive the powered signal that is persistent and in return, provide the reset signal.

15. (Original) The system of claim 13, wherein the conflict resolution module is operably connected to the host power terminal.
16. (Original) The system of claim 15, wherein the conflict resolution module is configured to activate the reset signal when the host computer is in a powered state.
17. (Original) The system of claim 13, wherein the device is selected from the list consisting of hard-disk drives, CD-ROM drives, WORM drives, and Bernoulli Drives.
18. (Original) The system of claim 13, further comprising a second host computer operating on the bus interface conforming to the SCSI standard and having a third device ID, the second host computer being connected to the bus interface subsequent to the first host computer.